

UNITED STATES DISTRICT COURT  
FOR THE DISTRICT OF DELAWARE

LG.PHILIPS LCD CO., LTD.,

Plaintiff,

v.

TATUNG COMPANY; TATUNG  
COMPANY OF AMERICA, INC.;  
CHUNGHWA PICTURE TUBES, LTD.;  
AND VIEWSONIC CORPORATION,

Defendants.

Civil Action No. 05-292 (JJF)

**REDACTED**

**DECLARATION OF GASPARE J. BONO IN SUPPORT OF  
PLAINTIFF LG.PHILIPS LCD COMPANY, LTD.'S  
ANSWERING BRIEF IN OPPOSITION TO DEFENDANTS' MOTION FOR  
PARTIAL SUMMARY JUDGMENT ON DAMAGES UNDER 35 U.S.C. § 287**

I, Gaspare J. Bono, hereby depose and state:

1. I am an attorney with the law firm of McKenna Long & Aldridge, L.L.P., 1900 K Street, N.W., Washington, D.C. 20006. McKenna Long & Aldridge, L.L.P. is counsel for Plaintiff LG.Philips LCD Company, Ltd. ("LPL") with respect to the instant action. I am admitted to the bars of the State of New York and the District of Columbia, and am admitted Pro Hac Vice in the above captioned case.

2. I make this Declaration in support of Plaintiff LG.Philips LCD Company, Ltd.'s Answering Brief in Opposition to Defendants' Motion for Partial Summary Judgment on Damages Under 35 U.S.C. § 287, filed contemporaneously with this Declaration. I am fully familiar with the facts contained therein and fully familiar with the documents described herein.

3. Attached as **Exhibit 1** is a true and correct copy of United States Patent No. 5,019,002.

4. Attached as **Exhibit 2** is a true and correct copy of excerpts from the deposition transcript of Webster E. Howard. The transcript reflects testimony given on July 1, 2006.

5. Attached as **Exhibit 3** is a true and correct copy of Defendant Chungwa Picture Tubes, Ltd.'s Disclosure Pursuant to Court Order Dated May 1, 2006.

6. Attached as **Exhibit 4** is a true and correct copy of excerpts from the deposition transcript of Youngwoo Cho. The transcript reflects testimony given on June 9, 2006.

7. Attached as **Exhibit 5** is a true and correct copy of excerpts from the deposition transcript of Ho Lee. The transcript reflects testimony given on July 3, 2006.

8. Attached as **Exhibit 6** is a true and correct copy of a Plaintiff LPL's Notice of Rule 30(b)(6) Deposition of Defendant Chungwa Picture Tubes, Ltd.

9. Attached as **Exhibit 7** is a true and correct copy of excerpts from the deposition transcript of CPT's Rule 30(b)(6) deposition designee Belle Chang. The transcript reflects testimony given on May 23, 2006.

10. Attached as **Exhibit 8** is a true and correct copy of Defendant CPT's Second Supplemental Response to Plaintiff's Amended First Set of Interrogatories.

11. Attached as **Exhibit 9** is a true and correct copy of excerpts from the deposition transcript of Chen Chao Tsai. The transcript reflects testimony given on June 6, 2006.

12. Attached as **Exhibit 10** is a true and correct copy of excerpts from the deposition transcript of Defendant Tatung Company's Rule 30(b)(6) deposition designee Oliver Shih. The transcript reflects testimony given on May 31, 2006.

13. Attached as **Exhibit 11** is a true and correct copy of excerpts from the deposition transcript of Defendant Viewsonic Corporation's Rule 30(b)(6) deposition designee Pearlyn Bee San Lim. The transcript reflects testimony given on June 22, 2006.

14. Attached as **Exhibit 12** is a true and correct copy of a document bates labeled TD-02944 to TD-02945, and produced by Defendant Tatung Company in this litigation.

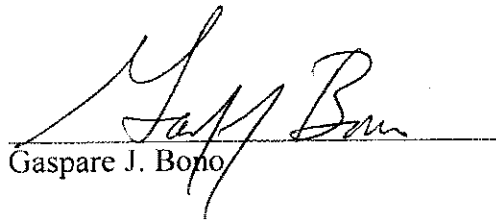
15. Attached as **Exhibit 13** is a true and correct copy of excerpts from the deposition transcript of Defendant Tatung Company's Rule 30(b)(6) deposition designee Pat Chang. The transcript reflects testimony given on June 8, 2006.

16. Attached as **Exhibit 14** is a true and correct copy of excerpts from the deposition transcript of Defendant Tatung Company of America, Inc.'s Rule 30(b)(6) deposition designee Mike Lee. The transcript reflects testimony given on June 23, 2006.

17. Attached as **Exhibit 15** is a true and correct copy of a document bates labeled VS-D007939 to VS-D008002, and produced by Defendant Viewsonic Company in this litigation.

18. I declare under the penalty of perjury that the foregoing is true and correct.

Executed on this 10th day of July, 2006.

  
Gaspare J. Bono

# Exhibit 1

# United States Patent [19]

Holmberg

[11] Patent Number: 5,019,002  
[45] Date of Patent: May 28, 1991

[54] METHOD OF MANUFACTURING FLAT PANEL BACKPLANES INCLUDING ELECTROSTATIC DISCHARGE PREVENTION AND DISPLAYS MADE THEREBY

[75] Inventor: Scott H. Holmberg, San Ramon, Calif.

[73] Assignee: Honeywell, Inc., Minneapolis, Minn.

[21] Appl. No.: 218,312

[22] Filed: Jul. 12, 1988

[51] Int. Cl.<sup>5</sup> ..... H01L 45/00

[52] U.S. Cl. .... 445/24; 357/23.13; 437/56

[58] Field of Search ..... 445/24, 3; 357/23.13, 357/4; 437/4, 8, 56

[56] References Cited

U.S. PATENT DOCUMENTS

4,455,739 6/1984 Hynecock ..... 427/8 X

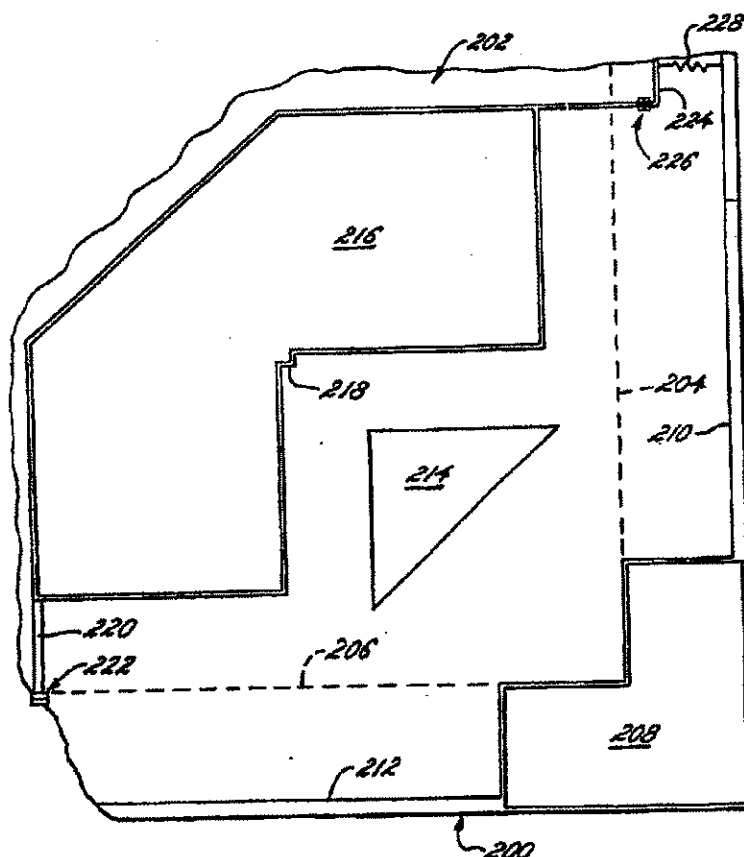
4,586,242 5/1986 Harrison ..... 437/8  
4,714,949 12/1987 Simmons et al. .... 357/23.13  
4,736,271 4/1988 Mack et al. .... 357/23.13  
4,803,536 2/1989 Tuan ..... 357/23.13

Primary Examiner—Kenneth J. Ramsey  
Attorney, Agent, or Firm—Leydig, Voit & Mayer

[57] ABSTRACT

Flat panel displays are provided including protection from electrostatic discharge (ESD) during manufacture and thereafter. At least one ESD guard ring is provided to protect the active elements of the display from the potential discharge between the row and column lines. An internal ESD guard ring is coupled to the row and column lines via shunt transistors. An external ESD guard ring is coupled to the row and column lines via a resistance. Both of the guard rings can be provided; however, the external guard ring is removed prior to completion of the display.

36 Claims, 5 Drawing Sheets



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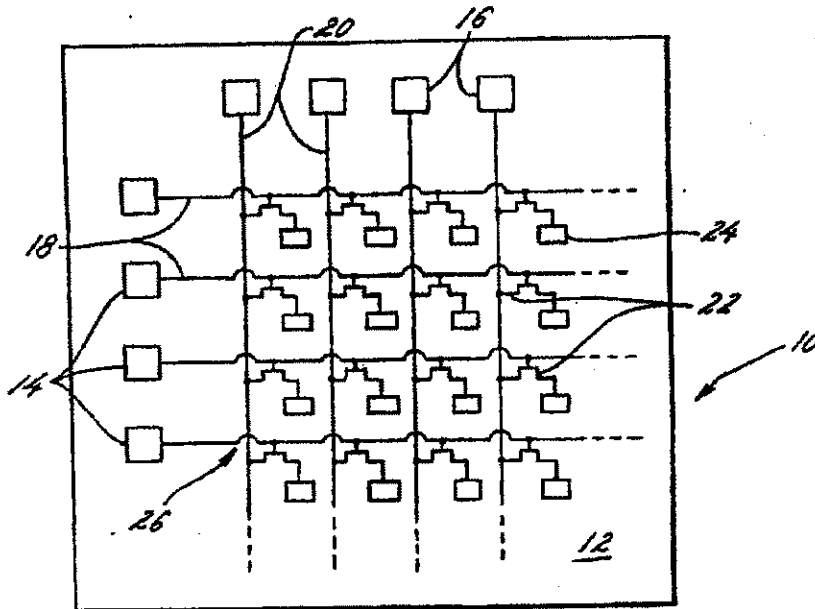


FIG. 1

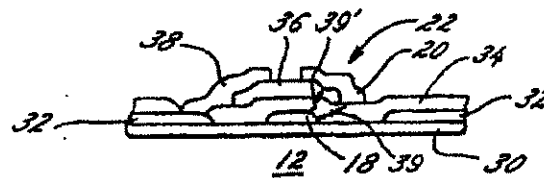
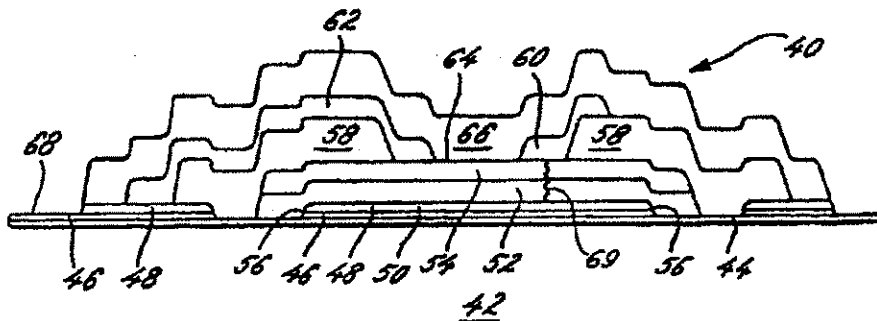


FIG. 2

FIG. 3.



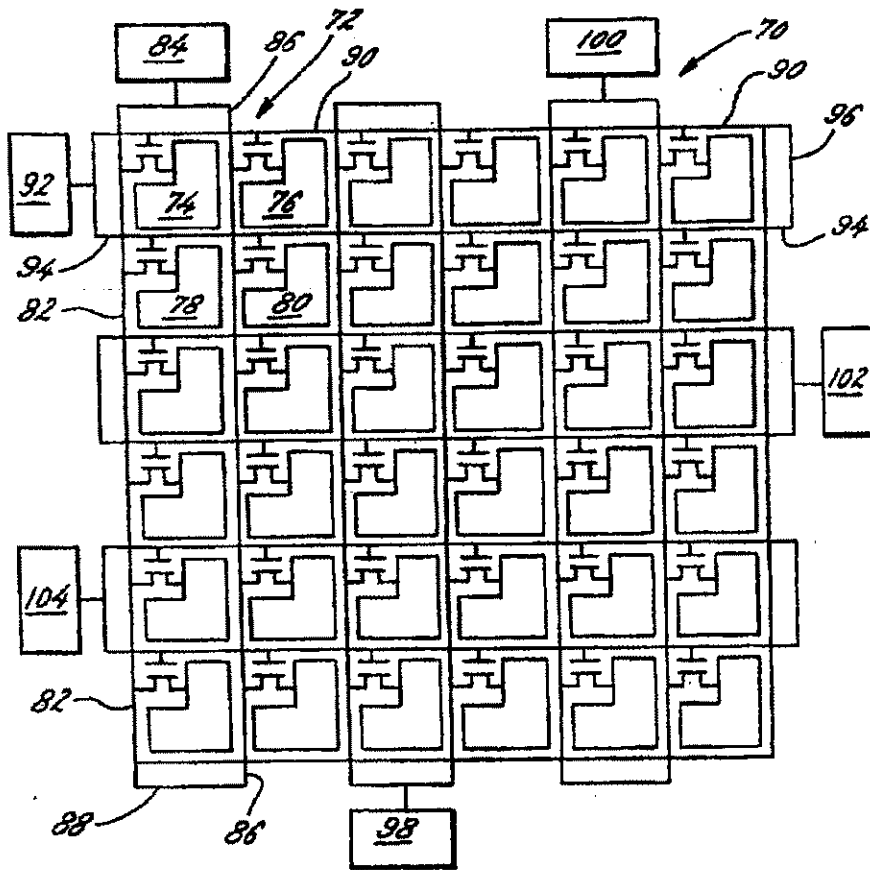
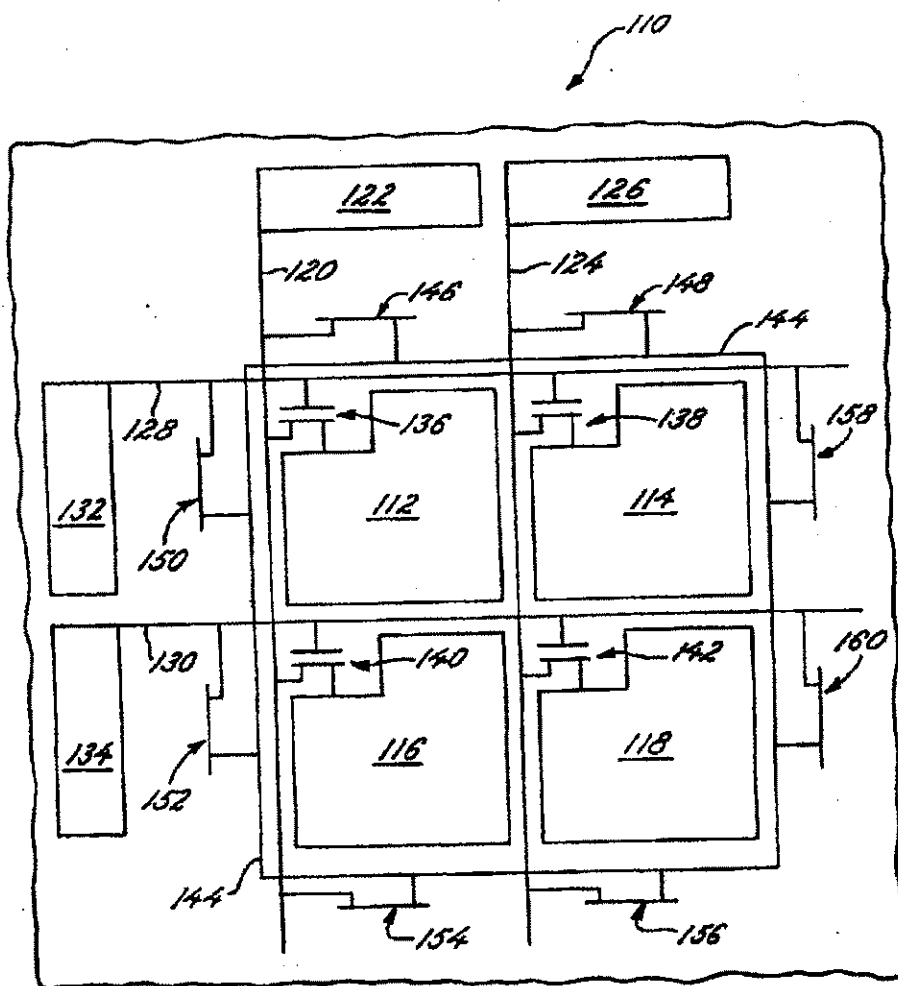


FIG. 4

FIG. 5



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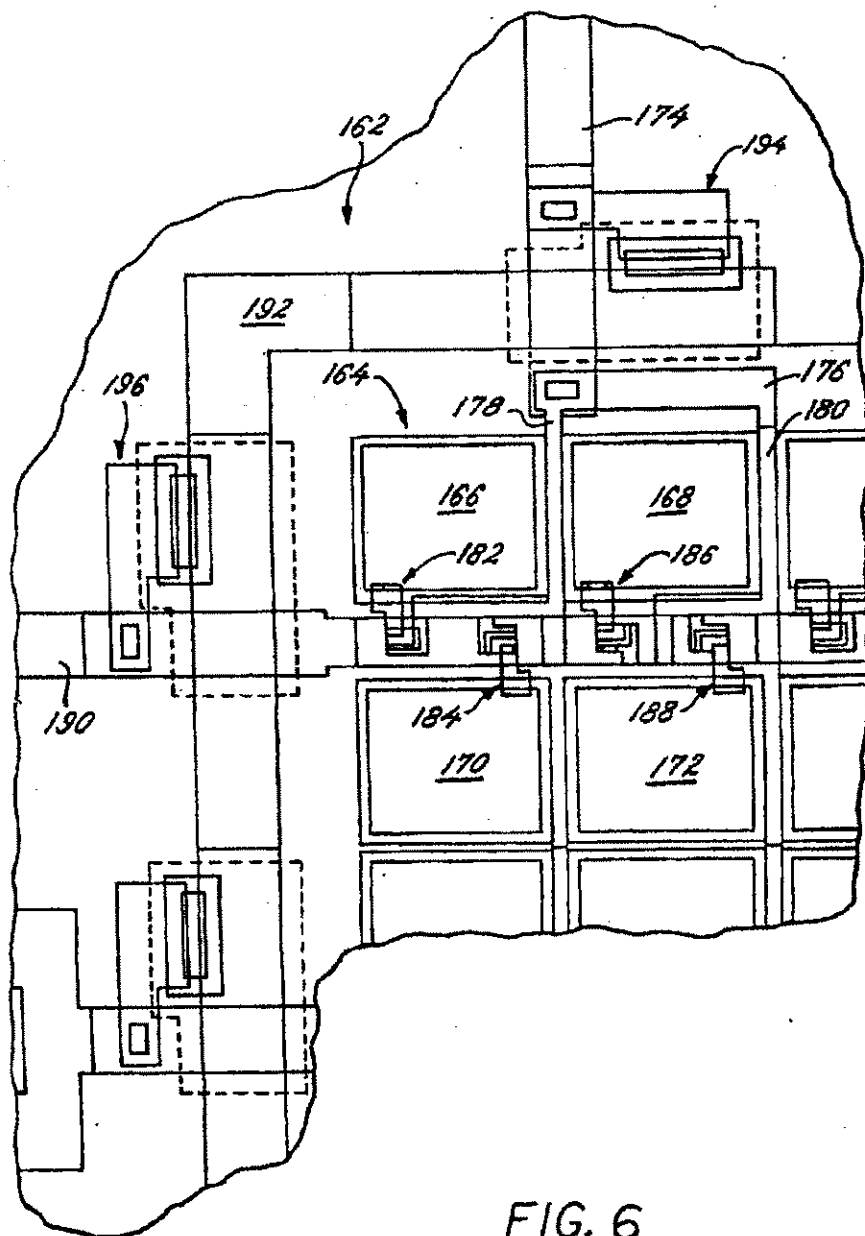


FIG. 6

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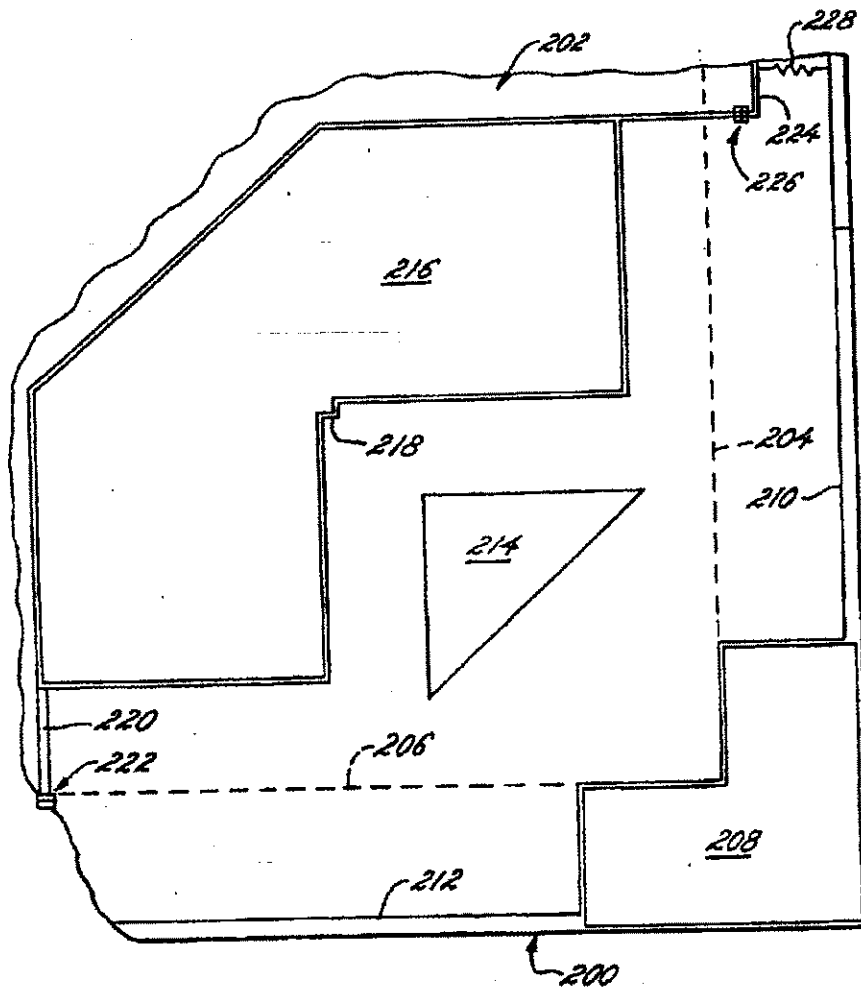


FIG. 7

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# METHOD OF MANUFACTURING FLAT PANEL BACKPLANES INCLUDING ELECTROSTATIC DISCHARGE PREVENTION AND DISPLAYS MADE THEREBY

## BACKGROUND OF THE INVENTION

The present invention pertains to improved flat panel displays and methods of making the displays with protection from electrostatic discharges. More particularly, the present invention is directed to methods of increasing the manufacturing yields of flat panel display backplanes and the displays made therefrom by improving handling characteristics.

In recent years there has been growing interest in flat panel displays, such as those which employ liquid crystals, electrochromic or electroluminescence, as replacements for conventional cathode ray tubes (CRT). The flat panel displays promise lighter weight, less bulk and substantially lower power consumption than CRT's. Also, as a consequence of their mode of operation, CRT's nearly always suffer from some distortion. The CRT functions by projecting an electron beam onto a phosphor-coated screen. The beam will cause the spot on which it is focused to glow with an intensity proportional to the intensity of the beam. The display is created by the constantly moving beam causing different spots on the screen to glow with different intensities. Because the electron beam travels a further distance from its stationary source to the edge of the screen than it does to the middle, the beam strikes various points on the screen at different angles with resulting variation in spot size and shape (i.e. distortion).

Flat panel displays are manufactured to be substantially free of such distortion. In the manufacture of flat panel displays the circuit elements are deposited and patterned, generally by photolithography, on a substrate, such as glass. The elements are deposited and etched in stages to build a device having a matrix of perpendicular rows and columns of circuit control lines with a pixel contact and control element between the control line rows and columns. The pixel contact has a medium thereon which is a substance that either glows (active) or changes its response to ambient light (passive) when a threshold voltage is applied across the medium control element. The medium can be a liquid crystal, electroluminescent or electrochromic materials such as zinc sulfide, a gas plasma of, for example, neon and argon, a dichroic dye, or such other appropriate material or device as will luminesce or otherwise change optical properties in response to the application of voltage thereto. Light is generated or other optical changes occur in the medium in response to the proper voltage applied thereto. Each optically active medium is generally referred to as a picture element or "pixel".

The circuitry for a flat panel display is generally designed such that the flat panel timeshares, or multiplexes, digital circuits to feed signals to one row and column control line of the pixels at a time. Generally one driving circuit is used for each row or column control line. In this way a subthreshold voltage can be fed to an entire row containing hundreds of thousands of pixels, keeping them all dark or inactive. Then a small additional voltage can be supplied selectively to particular columns to cause selected pixels to light up or change optical properties. The pixels can be made to glow brighter by applying a larger voltage or current of a longer pulse of voltage or current. Utilizing liquid

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crystal displays (LCD's) with twisted nematic active material, the display is substantially transparent when not activated and becomes light absorbing when activated. Thus, the image is created on the display by sequentially activating the pixels, row by row, across the display. The geometric distortion described above with respect to CRT's is not a factor in flat panel displays since each pixel sees essentially the same voltage or current.

One of the major problems that arises with respect to the prior art method of manufacture of backplanes for active matrix displays (e.g. those employing thin film transistors at each pixel) is that they generally suffer production yield problems similar to those of integrated circuits. That is, the yields of backplanes produced are generally not 100% and the yield (percentage of backplanes with no defects) can be 0% in a worst case. High quality displays will not tolerate any defective pixel transistors or other components. Also, larger size displays are generally more desirable than smaller size displays. Thus, a manufacturer is faced with the dilemma of preferring to manufacture larger displays, but having to discard the entire product if even one pixel is defective. In other words, the manufacturer suffers a radically increased manufacturing cost per unit resulting from decreasing usable product yield.

One solution to the low yield problem is disclosed in U.S. Ser. No. 948,224, filed Dec. 31, 1986, now U.S. Pat. No. 4,676,761 entitled "Method of Manufacturing Flat Panel Backplanes Including Improved Testing and Yields Thereof and Displays Made Thereby", which is owned by the assignee of the present application and is incorporated herein by reference.

These problems of increased cost and decreased yield are improved in the present invention by providing methods of manufacturing display backplanes and the resulting displays with electrostatic discharge protection which provide protection against fatal defects during and after manufacture of the displays.

## SUMMARY OF THE INVENTION

There is provided improved methods of manufacturing backplanes and the resulting flat panel displays to increase the manufacturing yield, decrease manufacturing costs and substantially eliminate fatal display defects caused by electrostatic discharge during manufacture and thereafter.

These improvements are accomplished by forming at least one electrostatic discharge (ESD) guard ring around the active elements of the display. An internal ESD guard ring can be formed, which provides a discharge path for static potential applied across the row and column line of the display. This prevents the potential from discharging between the row and column lines through an active element causing a short and resulting in a defect in the display during manufacture or thereafter. An external ESD guard ring can be formed, which provides protection during manufacture of the displays, however, the external ESD guard ring is removed at the end of the display manufacturing process. The displays also can include both the internal and external ESD guard ring to provide protection during manufacture and thereafter.

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## BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 is a plan view schematic representation of an active matrix display backplane made by a prior art method;

FIG. 2 is a cross-section of one transistor of the prior art backplane which could be utilized with the present invention;

FIG. 3 is a cross-section of one transistor which could be utilized with the present invention;

FIG. 4 is a plan view schematic representation of one prior embodiment of a subpixel matrix display;

FIG. 5 is a plan view schematic representation of a matrix display illustrating one embodiment of an internal ESD guard ring of the present invention;

FIG. 6 is an enlarged plan view of a portion of one embodiment of a subpixel matrix display illustrating the internal ESD guard ring in accordance with the present invention; and

FIG. 7 is a partial plan view of one embodiment of an exterior ESD guard ring of the present invention.

## DESCRIPTION OF THE PREFERRED EMBODIMENTS

Referring now more particularly to FIG. 1, there is shown a schematic representation of an active matrix flat panel display device 10 made in accordance with conventional photolithographic techniques. One such device 10 and the manufacture thereof is more fully described in Application of Amorphous Silicon Field Effect Transistors in Addressable Liquid Crystal Display Panels, A. J. Snell, et al., *Applied Physics*, No. 24, p. 357, 1981. The device 10 includes a substrate 12, sets of contact pads 14 and 16, sets of control or bus lines 18 and 20, and, in this particular example of the prior art, transistors 22 and pixel back contacts 24.

The substrate 12 commonly employed in these devices is formed from glass. The control lines 18 and 20 are organized into a matrix of rows 18 and columns 20. The control line rows 18 in this device 10 serve as gate electrodes and the control line columns 20 as source connections. One contact pad 14 is connected to one end of each of the row control lines 18. One contact pad 16 is connected to one end of each of the column control lines 20. The display drive control (not shown) is connected to the sets of pads 14 and 16.

At each matrix crossover point 26, where a row line 18 and a column line 20 cross, a switching element, transistor 22 is formed to connect the row line 18 and column line 20 to the pixel back contacts 24. The active medium is deposited at least on the contacts 24 which will optically change properties in response to the combined voltages or currents in the respective crossover point 26 formed by the row 18 and column 20. The active medium at a given crossover point 26 will appear as a square or dot in the overall checkerboard type matrix of the display 10. The actual size of the transistors 22 and the contacts 24 are not now drawn to scale, but are shown schematically for illustration only.

It should be noted that theoretically there is no limit on the number of rows 18 and columns 20 that can be employed, only a portion of which are illustrated in FIG. 1. Therefore, there is also no theoretical limit on the outside dimensions of such a device 10. However, the present state of the lithographic art places a practical limit on the outside dimensions of these devices. The present alignment techniques generally allow high resolution display devices to be manufactured approxi-

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mately five inches on a side 28, although improved techniques of up to fourteen inches on a side has been demonstrated.

The problem encountered by the prior art method of manufacture is that if the array of device 10 contains any defective pixel transistors 22 or other circuit elements causing a pixel to be inoperative, it must be discarded.

Referring in detail to FIG. 2, several problems occur when the switching element, transistor 22 is manufactured. The substrate 12 is a substantial portion of the backplane cost and hence an inexpensive soda-lime glass is generally utilized. It has been demonstrated by liquid crystal display manufacturers that the high sodium concentration can poison the liquid crystal materially diffusing through the overlying ITO layer and hence an  $\text{SiO}_2$  suppression layer 30 is generally formed on the substrate 12. There are some high quality low sodium types of substrates available, which would not need the suppression layer 30. An ITO layer 32 is formed and etched to provide an ITO free area on which the gate 18 is deposited. Following the deposition of the gate 18, a gate insulator layer 34 is deposited. Although a smooth uniform coverage of the gate 18 by the insulator 34 is illustrated, in production the gate 18 has or can have sharp edges which lead to pin holes or thinning of the insulator 34 at the gate edges. The source and drain metals can short to the gate 18. The thinning or pin holes produce transistors 22, which if operative, do not have uniform operating characteristics and hence the backplane is worthless.

One attempt to solve this problem, is to make the gate 18 very thin, but the resistivity is then too high to make the large arrays necessary for the backplane. A second attempt to solve the problem, is to make the gate insulator 34 very thick, but this decreases the gain of the transistor 22 and is also self defeating.

An amorphous silicon layer 36 is then deposited, with the source 20 and a drain 38 deposited thereover. A passivating layer (not shown) would be deposited over the completed structure to complete the transistor 22. During operation the activation of the source 20 and the gate 18 couples power through the silicon alloy 36 to the drain and hence to the contact pad 24 formed by the ITO layer 32.

During manufacture of the device 10, electrostatic discharge can occur when a high static electric potential is coupled across at least one pair of the gate lines 18 and the source lines 20. The discharge frequently will result in a short 39 through the insulator 34 or a short 39' through the insulator 34 and the silicon layer 36 in the transistor 22, between the adjacent crossover points of the lines 18 and 20 as can be seen in FIG. 2. This will cause at least one row and one intersecting column of the display pixels to be defective and in the type of display device 10, generally the defect will be a fatal one (clearly visible) and hence the device will be discarded. The device 10 does not provide any redundancy or subpixels and hence the defect cannot easily be isolated.

Referring now to FIG. 3, there is shown a schematic representation of one embodiment of a transistor 40 which can be utilized with the present invention. The transistor is more fully disclosed in U.S. Pat. Nos. 4,545,112 and 4,736,229, which are incorporated herein by reference.

A glass substrate 42 includes a barrier  $\text{SiO}_2$  layer 44 thereon. As above mentioned, a low sodium glass sub-

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strate, such as Corning 7059 glass, could be utilized, and hence the barrier layer 44 can be eliminated. The detailed deposition steps are described in the above-referenced patent and application. An ITO layer 46 is deposited and then a refractory metal layer 48 is deposited on the ITO layer 46.

The layers 46 and 48 are etched to form a gate electrode 50. A gate insulator 52 and a semiconductor material 54 are sequentially deposited over the gate 50. The material 54 preferably is an amorphous silicon alloy. To avoid the possibility of any gate to source or drain shorts at gate edges 56, a dielectric 58 is deposited over the gate 55, the gate insulator 52 and the semiconductor 54. The dielectric 58 is deposited to a sufficient thickness to ensure that no shorts or thin spots are formed between the edges 56 of the gate 50 and a source 60 and a drain 62 deposited thereover.

The dielectric 58 is etched away only on a substantially planar central region 64 of the semiconductor layer 54. This insures uniform operating characteristics for the transistors 40 in the backplane array. A passivating layer 66 is deposited over the whole structure to complete the structure of the transistor 40.

During all of the transistor processing steps, the refractory metal layer 48 remains over a pixel contact pad 68 upon which the active material of the pixel is deposited. As a final step, before the active medium (not shown) is added to the backplane to complete the display, the refractory metal is etched off of the pixel pad 68 leaving the ITO layer 46 exposed after all the processing has been completed.

The gate to source or drain shorts referred to above in discussing the dielectric 58, refer to physical shorts caused by thin spots or actual metal particles or filaments. The electrostatic discharges caused during manufacturing and thereafter will be deterred by the dielectric 58, but will not be eliminated. The potential can be high enough to again form a short 69 through the gate insulator 52 and the semiconductor material 54 in the transistor 40, between the source 60 and the gate 50. Depending upon the display structure, at least one pixel or one subpixel (FIG. 4) will be defective.

Referring now to FIG. 4, a subpixel matrix display of the above-referenced application, U.S. Ser. No. 948,224, is designated generally by the reference numeral 70. The subpixel matrix display 70 is illustrated as having each pixel subdivided into four subpixels, but the pixels could be subdivided into numerous other configurations such as two subpixels, two by four or six subpixels or in three subpixels for color applications. Each pixel 72 is subdivided into four subpixels 74, 76, 78 and 80 (only one pixel 72 is so numbered for illustration). As previously stated, the number of pixels is merely shown for illustration purposes and the display 70 could contain any desired number and configuration, square or rectangular.

A column (source) line or bus 82 connects the subpixels 74 and 78 and all other column subpixel pairs in one-half of each of the pixels to a column or source contact pad 84 at one edge of the display 70. A second column (source) line or bus 86 connects the subpixels 76 and 80 and all other column subpixel pairs in the second half of each of the pixels to the column or source contact pad 84. The bus lines 82 and 86 are interconnected (shorted) at or before the pad 84 and are interconnected (shorted) at the opposite ends by a line or short 88.

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A row (gate) line or bus 90 connects the subpixels 74 and 76 and all other row subpixel pairs in one-half of each of the pixels to a row (gate) pad 92. A second row (gate) line or bus 94 connects the subpixels 78 and 80 and all other row subpixel pairs in one-half of each of the pixels to the row pad 92. The bus lines 90 and 94 are interconnected (shorted) at or before the pad 92 and are interconnected (shorted) at the opposite ends by a line or short 96.

In a like manner, each of the other subpixel pairs are connected in columns to respective column (source) pads 98 and 100, etc. The pads 84, 98 and 100 are illustrated as being on opposite sides of the display to provide additional connecting space for the pads, however, they also could all be on one side as in the display 10. Each of the other subpixel pairs also are connected in rows to respective row (gate) pads 102 and 104, etc.

The pixel 72 then is divided into four subpixels 74, 76, 78 and 80 which allows for one of the subpixels to be defective, such as the subpixel 74, without causing a fatal defect, since the remaining three subpixels 76, 78 and 80 remain operative. In prior devices, the pixel 72 would be totally defective and hence the display 70 would be inoperable.

Further, one often fatal display defect is caused by a defect or open in one of the row or column bus lines which would cause the whole row or column to be out, again resulting in an inoperative display 70. With the respective subpixels pairs of row and column bus lines interconnected, however, an open in a bus line will at most cause one subpixel to be inoperative. An open in one or more of the bus lines between the subpixels will result in no defects, since the current is supplied from the opposite shorted end of the row or column bus line. Thus, the display 70 in effect has redundant row and column bus lines.

To avoid the fatal defect of the multiple open lines, as also disclosed in U.S. Ser. No. 948,224, the redundant row and column bus lines can be further interconnected at each subpixel. Each pair of the column bus lines 82 and 86 are additionally interconnected between each of the subpixels 74, 78, etc. by respective lines or shorts. In a like manner, each pair of the row bus lines 90 and 94 are interconnected between each of the subpixels 74, 76, etc. by respective lines or shorts. Further, although both the row bus lines and the column bus lines can be interconnected between each subpixel, only one of the row or the column bus line sets might be shorted to limit the loss of active pixel display area.

The short 69 in one of the active devices in the display 70 can be eliminated by opening the row or column line between the short and the line. This results in only one subpixel, such as the subpixel 74 being defective and due to the small size of the subpixel, is not a fatal defect (i.e. not readily visual). The rest of the corresponding column and row subpixels would be operable due to the redundant and interconnected row and column bus lines.

Referring now to FIG. 5, a matrix display incorporating one embodiment of an internal ESD guard ring of the present invention is designated generally by the reference numeral 110. The matrix display 110 is illustrated having four pixels 112, 114, 116 and 118. The pixels, however, can be subdivided into numerous subpixel configurations such as two or four subpixels, two by four or six subpixels or in three subpixels for color display applications. Also, as previously stated for the

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subpixel matrix display 70, the number of pixels can be of any number and configuration, square or rectangular.

A column (source) line or bus 120 connects the pixels 112 and 116 and all other pixels in the same column to a source contact pad 122 at one edge of the display 110. A source line 124 connects the pixels 114 and 118 to a source contact pad 126. In a like manner, a pair of row (gate) lines 128 and 130 connect respective pairs of pixels 112, 114 and 116, 118 in each row to respective gate pads 132 and 134.

Each pixel 112, 114, 116 and 118 includes a respective active element, such as transistors 136, 138, 140 and 142 which couple the pixels to the respective source lines 120 or 124 and gate lines 128 or 130. To prevent a large electrostatic potential discharging through one of the transistors 136, 138, 140 and 142, an internal ESD guard ring 144 is formed around the pixels 112, 114, 116 and 118. The guard ring 144 is illustrated as a closed ring, but could also be an open L or C-shaped line if the gate and source pads all are on one respective side of the display 110.

The ESD guard ring 144 also is coupled via respective transistors 146, 148, 150 and 152 to, the source and gate lines. The guard ring 144 will be coupled to the end of each source and gate line, so if the source and gate lines include pads at their opposite ends (not illustrated), then the guard ring 144 will include a further respective set of transistors 154, 156, 158 and 160.

The ESD guard ring 144 preferably is formed from a low resistance metal, such as an aluminum alloy. The transistors 146 through 160 can include a floating gate (not illustrated), no gate, or can include an oxide below to form a spark gap.

In operation, with the guard ring 144, a potential placed upon the source pads 122 will not short one of the transistors 136 or 140. Instead, the transistor 146 will turn on followed by the transistor 150, shorting the potential from the pad 122, via the line 120, the transistor 146, the guard ring 144, the transistor 150 and the line 128 to the pad 132. Thus, the guard ring 144 will not allow high potentials across the pads 122, 126, 132 and 134. The guard ring 144 preferably is formed concurrently with the display elements and is not removed, providing continuous protection even following manufacture of the display 110.

A specific subpixel display incorporating an internal guard ring of the invention is best illustrated in FIG. 6 and is designated generally by the reference numeral 162. The display 162 includes a plurality of pixels, each having four subpixels in a similar fashion to the display 70 illustrated in FIG. 4. Only one pixel 164 is illustrated in detail and includes four subpixels 166, 168, 170 and 172. A source line 174 includes a shorting line 176 which is connected to a pair of source lines 178 and 180, coupled to each of the subpixels by a respective transistor structure 182, 184, 186 and 188, which are not described in detail. The transistors 182, 184, 186 and 188 also couple the subpixels 166, 168, 170 and 172 to a gate line 190.

An internal ESD guard ring 192 is coupled via a transistor structure 194 to the source line 174 and via a transistor structure 196 to the gate line 190. The guard ring 192 and transistors 194 and 196 operate as before described to short any potential to ground. The low value of the normal operating voltages does not turn on the transistors 194 and 196, which do not effect the normal display operation.

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The ESD preventive structure can also include an outer ESD guard ring 200, best illustrated in FIG. 7. Only one corner portion 202 of the display and guard ring 200 is illustrated. While the display is being manufactured, the outer guard ring 200 is connected to all of one of the source and gate pads (not illustrated), which pads are serially connected together via jumpers outside of scribe lines 204 and 206. A corner pad 208 is connected to each other corner pad (not illustrated) by respective outer conductive lines 210 and 212 of the guard ring 200. The L-shaped corner pad 208 can be grounded and also provides the alignment for the scribe lines 204 and 206, which are utilized to disconnect the source and gate jumpers and the guard ring 200 after the structure is completed. The corner portion 202 includes a triangular pad 214 which provides alignment for diagonal corner displays, when utilized.

A backplane pickup contact pad 216 also is provided, which includes a corner 218 for aligning the backplane with the front plane. The pad 216 includes a shunt line 220 which is connected to one set of source or gate lines via a shunt transistor 222 along the edge to be scribed and removed along the line 206. The line 210 is connected to the other set of gate or source lines by a shunt line 224, a shunt transistor 226 and a large resistance 228, such as 100 K ohms (illustrated schematically). The outer ESD guard ring 200 provides ESD protection only during manufacture of the display and is removed prior to completion of the display. The resistance 228 provides an ESD short for high electrostatic potentials, which can be incurred during manufacturing of the display which can be connected anywhere between the line 210 and the other set of gate or source lines. The resistance 228 minimizes the discharge current surge and the shunt transistors 222 and 226 act as before described. There will be at least one corner backplane pickup pad 216 and preferably there will be two or three, each with their associated shunt transistors.

The outer guard ring lines 210 and 212 preferably are formed at the same time as the first of the gate or source lines. The inner guard ring 44 and the associated shunt transistors of both guard rings preferably are formed concurrently with the other display structures. The scribe lines 204 and 206 can be prescribed, but left intact until the back and front planes are mated and then removed to provide the gate and source contacts for the printed circuit board connections.

Modification and variations of the present invention are possible in light of the above teachings. The transistors 22 or other types of two or three terminal switching devices can be utilized with the invention. The amorphous silicon alloy semiconductor material 54, could be any of numerous types of materials such as CdSe or GaAs materials. The ESD guard rings can be utilized separately or together with all types of active element matrix displays and not just those illustrated. The shunt transistors 146, 194 and 222, etc. also can be formed as other active switching elements, such as diodes. It is therefore to be understood that within the scope of the appended claims, the invention may be practiced otherwise than as specifically described.

What is claimed and desired to be secured by Letters Patent of the United States is:

1. A method of manufacturing active matrix display backplanes and displays therefrom, comprising:
  - providing a substrate;
  - forming a pattern of pixels on said substrate;

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forming a plurality of row and column intersecting pixel activation lines, interconnecting substantially all of said row lines to one another and substantially all of said column lines to one another;

forming an outer electrostatic discharge guard ring on said substrate coupled to said interconnected row and column lines via a resistance to provide protection from electrostatic discharges between said row and column activation lines during manufacture of the displays; and

removing said outer guard ring and row and column interconnections prior to completion of the display.

2. The method as defined in claim 1 including coupling one plurality of said interconnected row and column lines to said outer guard ring via said resistance.

3. The method as defined in claim 2 including forming at least one pickup pad coupled to said resistance via a shunt switching element.

4. The method as defined in claim 3 including coupling said pickup pad to the other plurality of said interconnected row and column lines via another shunt switching element.

5. The method as defined in claim 3 including forming a corner on said pad to align the front plane and back plane of the display.

6. The method as defined in claim 3 including forming a plurality of pickup pads, each one on a separate corner of the display.

7. The method as defined in claim 1 including forming a corner pad on at least one corner of the display and aligning scribe lines with said corner pad for removing said outer guard ring and row and column intersections.

8. The method as defined in claim 1 including forming an inner electrostatic discharge guard ring on said substrate coupled to said row and column lines via shunt switching elements to provide protection from electrostatic discharges between said row and column activation lines during manufacture of the displays and thereafter.

9. The method as defined in claim 8 including forming separate shunt switching elements between said inner guard ring and each row and column line.

10. A method of manufacturing active matrix display backplanes and displays therefrom, comprising:

providing a substrate;

forming a pattern of pixels on said substrate; forming a plurality of row and column intersecting pixel activation lines; and

forming an inner electrostatic discharge guard ring on said substrate coupled to said row and column lines via shunt switching elements to provide protection from electrostatic discharges between said row and column activation lines during manufacture of the displays and thereafter.

11. The method as defined in claim 10 including forming separate shunt switching elements between said inner guard ring and each row and column line.

12. The method as defined in claim 10 including interconnecting substantially all of said row lines to one another and substantially all of said column lines to one another and forming an outer electrostatic discharge guard ring on said substrate coupled to said interconnected row and column lines via a resistance to provide protection from electrostatic discharges between said row and column activation lines during manufacture of the displays; and

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removing said outer guard ring and row and column interconnections prior to completion of the display.

13. The method as defined in claim 12 including coupling one plurality of said interconnected row and column lines to said outer guard ring via said resistance.

14. The method as defined in claim 13 including forming at least one pickup pad coupled to said resistance via a shunt switching element.

15. The method as defined in claim 14 including coupling said pickup pad to the other plurality of said interconnected row and column lines via another shunt switching element.

16. The method as defined in claim 14 including forming a corner on said pad to align the front plane and back plane of the display.

17. The method as defined in claim 10 including forming a plurality of pickup pads, each one on a separate corner of the display.

18. The method as defined in claim 10 including forming a corner pad on at least one corner of the display and aligning scribe lines with said corner pad for removing said outer guard ring and row and column intersections.

19. An active matrix display backplane, comprising: a substrate;

a pattern of pixels formed on said substrate;

a plurality of row and column intersecting pixel activation lines, substantially all of said row lines interconnected to one another and substantially all of said column lines interconnected to one another; and

an outer removable electrostatic discharge guard ring formed on said substrate coupled to said interconnected row and column lines via a resistance to provide protection from electrostatic discharges between said row and column activation lines during manufacture of the displays.

20. The backplane as defined in claim 19 including one plurality of said interconnected row and column lines coupled to said outer guard ring via said resistance.

21. The backplane as defined in claim 20 including at least one pickup pad coupled to said resistance via a shunt switching element.

22. The backplane as defined in claim 21 including said pickup pad coupled to the other plurality of said interconnected row and column lines via another shunt switching element.

23. The backplane as defined in claim 21 including a corner formed on said pad to align the front plane and back plane of the display.

24. The backplane as defined in claim 21 including a plurality of pickup pads, each one formed on a separate corner of the display.

25. The backplane as defined in claim 19 including a corner pad formed on at least one corner of the display and having scribe lines aligned with said corner pad for removing said outer guard ring and row and column intersections.

26. The backplane as defined in claim 19 including an inner electrostatic discharge guard ring formed on said substrate coupled to said row and column lines via shunt switching elements to provide protection from electrostatic discharges between said row and column activation lines during manufacture of the displays and thereafter.

27. The backplane as defined in claim 26 including separate shunt switching elements formed between said inner guard ring and each row and column line.

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28. An active matrix display backplane, comprising:  
a substrate;  
a pattern of pixels formed on said substrate;  
a plurality of row and column intersecting pixel activation lines; and

an inner electrostatic discharge guard ring formed on said substrate coupled to said row and column lines via shunt switching elements to provide protection from electrostatic discharges between said row and column activation lines during manufacture of the displays and thereafter.

29. The backplane as defined in claim 28 including separate shunt switching elements formed between said inner guard ring and each row and column line.

30. The backplane as defined in claim 28 including substantially all of said row lines interconnected to one another and substantially all of said column lines interconnected to one another and an outer electrostatic discharge guard ring formed on said substrate coupled to said interconnected row and column lines via a resistance to provide protection from electrostatic dis-

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charges between said row and column activation lines during manufacture of the displays.

31. The backplane as defined in claim 30 including one plurality of said interconnected row and column lines coupled to said outer guard ring via said resistance.

32. The backplane as defined in claim 31 including at least one pickup pad coupled to said resistance via a shunt switching element.

33. The backplane as defined in claim 32 including said pickup pad coupled to the other plurality of said interconnected row and column lines via another shunt switching element.

34. The backplane as defined in claim 32 including a corner formed on said pad to align the front plane and back plane of the display.

35. The backplane as defined in claim 28 including a plurality of pickup pads, each one formed on a separate corner of the display.

36. The backplane as defined in claim 28 including a corner pad formed on at least one corner of the display and having scribe lines aligned with said corner pad for removing said outer guard ring and row and column intersections.

\* \* \* \* \*

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UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 5,019,002

DATED : May 28, 1991

INVENTOR(S) : Scott H. Holmberg

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Col. 2, lines 30-31, change "4,676,761" to  
--4,820,222--;

Col. 4, line 15, change "materially" to  
--material by--;

Col. 5, line 53, change "30" to --80--;  
line 59, change "all" to --all--;

Col. 7, line 23, delete the third comma;

Col. 8, line 41, change "firs;" to --first--.

Signed and Sealed this  
Twenty-third Day of February, 1993

Attest:

STEPHEN G. KUNIN

Attesting Officer

Acting Commissioner of Patents and Trademarks

# Exhibit 2

## CONFIDENTIAL DOCUMENT

These pages have been removed because they contain confidential information subject to the Protective Order.

# Exhibit 3

## CONFIDENTIAL DOCUMENT

These pages have been removed because they contain confidential information subject to the Protective Order.

# Exhibit 4

## CONFIDENTIAL DOCUMENT

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# Exhibit 5

## CONFIDENTIAL DOCUMENT

These pages have been removed because they contain confidential information subject to the Protective Order.

# Exhibit 6

IN THE UNITED STATES DISTRICT COURT  
FOR THE DISTRICT OF DELAWARE

LG.PHILIPS LCD CO., LTD.,

Plaintiff,

v.

TATUNG COMPANY;  
TATUNG COMPANY OF AMERICA, INC.;  
CHUNGHWA PICTURE TUBES, LTD.;  
AND VIEWSONIC CORPORATION,

Defendants.

Civil Action No. 05-292 (JJF)

**NOTICE OF RULE 30(b)(6) DEPOSITION  
OF CHUNGHWA PICTURE TUBES, LTD.**

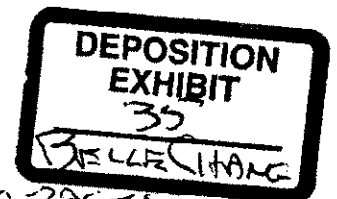
TO:

Robert W. Whetzel, Esq.  
Matthew W. King, Esq.  
Richards, Layton & Finger  
One Rodney Square  
P.O. Box 551  
Wilmington, DE 19899

Christine A. Dudzik, Esq.  
Thomas W. Jenkins, Esq.  
Howrey LLP  
321 North Clark Street  
Suite 3400  
Chicago, IL 60610

Teresa M. Corbin, Esq.  
Glenn W. Rhodes, Esq.  
Howrey LLP  
525 Market Street  
Suite 3600  
San Francisco, CA 94105

PLEASE TAKE NOTICE that Plaintiff LG.Philips LCD Co., Ltd. ("LPL") will take the deposition of Defendant Chunghwa Picture Tubes, Ltd. ("CPT"), pursuant to Fed. R. Civ. P. 30(b)(6), beginning on Tuesday, February 28, 2006, at 9:00 a.m. and continuing through Friday March 3, 2006. The deposition will take place at The Bayard Firm, 222 Delaware Avenue, 9th Floor, Wilmington, DE 19899-5130. The deposition will be taken before a notary public or court reporter, duly authorized to administer oaths



and transcribe the testimony of the deponent(s). The deposition will be videotaped and will continue from day to day until completed or adjourned if authorized by the Court or stipulated by the parties. The subjects covered in the deposition will include (but are not limited to) the subjects listed on Attachment A to this Notice.

Pursuant to Fed. R. Civ. P. 30(b)(6), Chunghwa Picture Tubes, Ltd. is required to designate one or more persons to testify at the deposition as to the matters known or reasonably available to Chunghwa Picture Tubes, Ltd.

December 14, 2005

THE BAYARD FIRM

/s/ Richard D. Kirk (rk0922)  
222 Delaware Avenue, Suite 900  
P.O. Box 25130  
Wilmington, DE 19899-5130  
(302) 429-4208

Counsel for Plaintiff  
LG.PHILIPS LCD CO., LTD.

OF COUNSEL:

Gaspare J. Bono  
Matthew T. Bailey  
Andrew J. Park  
McKenna Long & Aldridge LLP  
1900 K Street, NW  
Washington, D.C. 20006  
(202) 496-7500

**ATTACHMENT A**

**DEFINITIONS**

For purposes of this Attachment, you should use the following definitions for the terms used herein. These definitions of terms are for purposes of discovery only, and not to be construed as limiting or reflecting LPL's position in this case regarding claim construction or any other issue. LPL specifically reserves the right to use different terms, or to assert different meanings, for all purposes in this case (including, for example, with respect to claim construction, infringement analysis, and validity analysis).

1. "CPT," "you," and "your" as used herein, means Defendant Chunghwa Picture Tubes, Ltd. and all persons or entities acting or purporting to act on your behalf, and any of your affiliates, including, for example, Tatung Company.
2. "Plaintiff" refers to the Plaintiff in the above-captioned lawsuit as well as its affiliates.
3. "The '121 Patent" means United States Patent No. 6,738,121, entitled "Tape carrier package with dummy bending part and liquid crystal display employing the same," attached as Exhibit A to the Plaintiff's Complaint in Civil Action No. 05-292 (JJF).
4. "The '002 Patent" means United States Patent No. 5,019,002, entitled "Method of manufacturing flat panel backplanes including electrostatic discharge prevention and displays made thereby," attached as Exhibit B to the Plaintiff's Complaint in Civil Action No. 05-292 (JJF).

5. "Patents-in-Suit" refers either individually or collectively to United States Patent No. 6,738,121 and United States Patent No. 5,019,002.

6. "Affiliate(s)" means any corporation or other entity that controls, is controlled by or is under common control with the identified corporation or entity, including without limitation partnerships, parents, subsidiaries and divisions.

7. "Any" means each and every.

8. The connectives "and," "or," and "and/or" shall be construed either disjunctively or conjunctively as necessary to bring within the scope of these interrogatories (or, if applicable, document requests) all information that might otherwise be construed to be outside of their scope.

9. "Communication" means, without limitation, every manner or means of statement, utterance, notation, disclaimer, transfer or exchange of information between two or more persons of any nature whatsoever, by or to whomever, whether oral or written or whether face-to-face, by telephone, email, mail, personal delivery or otherwise, including but not limited to, e-mails, letters, correspondence, conversations, memoranda, dialogue, discussions, meetings, interviews, consultations, agreements and other understandings.

10. "Concern" and "concerning" are used in their broadest sense and embrace all matter relating to, referring to, describing, discussing, evidencing or constituting the referenced subject.

11. "Discuss," "discussing," "relate to," "relating to," "support," or "supporting" means in any way directly or indirectly, in whole or in part, discussing,

referring to, regarding, constituting, concerning, about, pertaining to, relating to, reflecting, considering, underlying, modifying, amending, confirming, mentioning, endorsing, evidencing, summarizing, memorializing, describing, discussing, analyzing, evaluating, representing, supporting, qualifying, terminating, revoking, canceling, or negating.

12. "Document" means all types of documents and things embraced within Federal Rule of Civil Procedure 34 and includes, without limitation, any writing and each original, or a copy in the absence of the original, and every copy bearing notes or markings not present on the original or copy, of the following items, however produced or reproduced, namely: books, accounting records of any nature whatsoever, agreements, communications, correspondence, facsimiles, telegrams, emails, cable, telexes, memoranda, recordings, studies, summaries or records of telephone conversations, summaries or records of personal conversations or interviews, diaries, letters, forecasts, statistical statements, graphs, laboratory or engineering reports and records, notebooks, charts, plans, sketches, drawings, video tapes, films, slides, information bearing photographic products of any nature whatsoever, photo-records, microfilms, tape recordings, minutes or records of meetings or conferences, expressions or statements of policy, lists of persons attending meetings or conferences, reports or summaries of interviews, reports or summaries of investigations, opinions or reports of consultants, patent studies, or opinions of counsel, records, reports or summaries of negotiations, sales literature of any nature whatsoever, brochures, catalogues, catalogue sheets, pamphlets, periodicals, advertisements, circulars or trade letters, press releases, trade releases, publicity releases, new product releases, reprints, drafts of any documents,

working papers, indices, original or preliminary notes, computer printouts, floppy disks, hard drives, CD-ROMs, magnetic tapes and other data compilations from which information can be obtained or translated, if necessary by the defendant through detection devices into reasonably usable form. The term document also refers to any tangible object other than a document as described above, and includes objects of every kind and nature such as, but not limited to, prototypes, models, and specimens.

13. "Identify" used in respect to a company or corporate entity means to set forth:

- A. the full name of the company;
- B. the full name of the division or office involved, if applicable; and
- C. the address of the company and of the division or office involved, if applicable.

14. "Identify" used in respect to a document or thing means:

- A. to provide a brief description of such document or thing, including date, author, recipients, type, and content or substance;
- B. to identify the custodian of the document or thing;
- C. to identify the place where the document or thing may be inspected; and
- D. if a copy of the document has been supplied to Plaintiff, to so state and specifically identify the copy supplied by reference to production numbers or other identifying information.

15. "Identify" used with respect to a natural person means to state:

- A. the full name;
- B. the present or last known business and residence addresses;
- C. the last known employer or job affiliation; and
- D. the last known occupation and business position or title held.

As to present or former government employees, state the government employer for each position held, the starting and ending date for each position, and the job description and title for each position held. Also, state the person's responsibilities with regard to any contract specified in the interrogatory.

16. "Import" or "importation" has the same meaning as in 35 U.S.C. § 271 and applicable case law.

17. "LCD display module" means the module including an LCD display panel, a backlight unit, driver ICs, and any related components.

18. "LCD display panel" means two substrates with liquid crystal material between the substrates.

19. "LCD display product" means any device that contains an LCD display module. An LCD computer monitor, LCD television, laptop computer, and any telephone or other portable or handheld product incorporating an LCD display module are examples of LCD display products. This includes all such devices, regardless of brand name, and thus includes, but is not limited to, Tatung brand and ViewSonic brand products.

20. "Machine" has the same meaning as in U.S. patent law and applicable case law.

21. "Make" has the same meaning as in 35 U.S.C. § 271 and applicable case law.

22. "Manufacture" has the same meaning as in U.S. patent law and applicable case law.

23. "Offer to sell" has the same meaning as in 35 U.S.C. § 271 and applicable case law.

24. "Part number" means a name or alpha-numeric identifier by which a part or product, for example an LCD display product, LCD display module, LCD display panel, or components thereof, in general, can be bought, sold, or identified.

25. "Person" means any natural person, firm, association, partnership, corporation, or other form of legal entity.

26. "Prior art" has the same meaning as in U.S. patent law, including in 35 U.S.C. §§ 102 and 103, and applicable case law.

27. "Process" has the same meaning as in U.S. patent law and applicable case law.

28. "Sell" or "sale" has the same meaning as in 35 U.S.C. § 271 and applicable case law.

29. "Specification" means anything that discusses, illustrates, defines, prescribes, or present procedures concerning, in whole or in part, any aspect of how

something can or will be made, inspected, or tested. Specifications include, for example, textual documents, drawings, photographs, blueprints, and/or illustrations.

30. "Type of LCD display product," "Type of LCD display module," and "Type of LCD display panel" means the name or alpha-numeric identifier by which a given LCD display product, module, or panel is identified. This includes, but is not limited to a part number, product code, material code, stock number, bar code number, or any other type of alpha-numeric identifier by which an item is bought, sold, or identified.

31. The use of the singular form of any word includes the plural and vice versa.

32. "Used" has the same meaning as in 35 U.S.C. § 271 and applicable case law.

#### **TOPICS TO BE ADDRESSED AT THE DEPOSITION**

1. Each type of LCD display module made, used, sold, and/or offered for sale, by or for you, each month since May 13, 1999.

2. Each type of LCD display panel made, used, sold, and/or offered for sale, by or for you, each month since May 13, 1999.

3. The quantity and price (including suggested resale and retail prices) of each type of LCD display module and LCD display panel made, used, sold, and/or offered for sale, by or for you, each month since May 13, 1999.

4. The design, development, making, assembly, production, testing, inspection, and/or qualification of LCD display modules and LCD display panels made,

bought, or sold, by or for you since May 13, 1999, including but not limited to optical, mechanical, and electrical testing and inspection.

5. All documents associated with the conception, design, and development of your LCD display modules and LCD display panels, including, for example, design or engineering documentation.

6. All specifications relating to your LCD display modules and LCD display panels, including, but not limited to, all specifications related to tape carrier packages and all specifications related to circuits and/or structures concerning electrostatic discharge protection of thin film transistors of LCD panels.

7. Your contentions regarding whether or not you are infringing the Patents-in-Suit, whether directly, under the doctrine of equivalents, or otherwise.

8. Your contentions regarding the validity and/or enforceability of the Patents-in-Suit, including, for example, your contentions regarding "an invalidating act that occurred more than a year prior to the filing date of the asserted patent."

9. All prior art that you contend is relevant to the Patents-in-Suit, including, for example, any prior art that you contend invalidates the Patents-in-Suit, individually or collectively.

10. The date that you first became aware of the Patents-in-Suit and the circumstances surrounding your first knowledge of the Patents-in-Suit, including, but not limited to, the date of your first actual notice of the Patent-in-Suit, the form of such notice, the source of such notice, identification of the person employed by you who first learned of the Patents-in-Suit and from what source, explaining the manner in which that person learned of the Patents-in-Suit, and identifying all documents that reflect when

and how you learned of the Patents-in-Suit, any discussions internal to you regarding the Patents-in-Suit, communications with third parties (including vendors, customers, or others) regarding the Patents-in-Suit, actions you took upon becoming aware of the Patents-in-Suit, including, without limitation, product modification(s), design-around efforts, conducting non-infringement and/or invalidity investigation(s), and/or seeking advice of counsel.

11. Your efforts to avoid infringing the Patents-in-Suit since the time that you first learned of the Patents-in-Suit, including, but not limited to all attempts, by or for you, to design around the Patents-in-Suit.

12. The efforts you took to gather and search for information and documents responsive to the Interrogatories and Requests for Production of Documents and Things, propounded to you by Plaintiff, including the locations for responsive information and documents, the persons interviewed regarding the responsiveness of such information and documents, and the manner in which you searched for electronic information, documents or records.

13. The information and documents referenced in your answers to Interrogatories propounded to you by Plaintiff.

14. The types and categories of documents that you receive, create, generate, or maintain concerning the manufacture, importation, purchase, distribution, sale, offer for sale, receipt, or advertisement of your LCD display modules and LCD display panels.

15. Your worldwide sales or shipments of LCD display modules and LCD display panels to any persons, including the amount of revenue and profit you have

derived from your LCD display modules and LCD display panels and sales or shipments to the United States.

16. The relationship between or among you and all entities that distribute, supply, market, import, purchase, and/or sell LCD display modules and LCD display panels manufactured, distributed, or sold, by or for you, the identity of all such entities, any contracts, agreements, or contractual or business arrangements between you and such entities, and the quantity and value of LCD display modules and LCD display panels involved.

17. All distributors, suppliers, marketers, importers/exporters, purchasers, sellers, and channels or networks through which you have, directly or indirectly, distributed, supplied, marketed, shipped, imported, exported, transferred, purchased, and/or sold LCD display modules and/or LCD display modules, including such distribution, supply, marketing, importing/exporting, purchasing, selling in the United States.

18. Your organizational structure, including but not limited to all persons involved in the research, design, manufacture, marketing, distribution, importation, exportation, purchase, and sale of your LCD display modules and LCD display panels, and each person's respective role and place in the chain of distribution of any consumer goods or devices containing or relating to such LCD display modules and LCD display panels.

19. Your forecasts or projections of sales, revenues from, and profitability of your LCD display modules and LCD display panels and your actual sales, revenues

from, and profitability of your LCD display modules and LCD display panels, since May 13, 1999.

20. All efforts you have taken to sell or distribute your LCD display modules and LCD display panels, and all efforts you have taken to promote, market, and/or advertise your LCD display modules, LCD display panels, and your company, including any parents, subsidiaries, and affiliates.

21. Meetings, communications, agreements, and contractual and business relationships, that relate to the importation, manufacture, distribution, promotion, marketing, advertising, purchases, or sales of any of your LCD display modules and LCD display panels.

22. Communications and/or participation in DisplaySearch and/or the Standard Panels Working Group that relate to the importation, manufacture, distribution, marketing, advertising, purchase, or sale of any of your LCD display modules and LCD display panels.

23. All licenses, royalties, and/or technology transfer agreements that pertain to LCD display modules and/or LCD display panels to which you are a party, assignor, assignee, or beneficiary, as well as any policies, practices, or relevant factors you have used when negotiating potential license agreements.

24. All contracts and related documents reflecting each order from and sale to United States customers of any types of LCD display modules and LCD display panels made, sold, or offered for sale, by or for you, since May 13, 1999.

25. All reports and data compilations showing actual monthly sales, by or for you, concerning each type of LCD display module and LCD display panel made, sold, or offered for sale, by or for you, since May 13, 1999.

26. For all types of LCD display modules and LCD display panels made, sold, or offered for sale, by or for you, since May 13, 1999, documents sufficient to determine the quantity of each type of LCD display module and LCD display panel that you made, manufactured, shipped, and/or sold each month, and the amount of revenue generated each month for all such shipments and sales since May 13, 1999, including, for example, the per unit gross profit, net profit, and profit margin on each unit sold or transferred.

27. For all types of LCD display modules and LCD display panels made, sold, or offered for sale, by or for you, since May 13, 1999, documents sufficient to determine the quantity of each type of LCD display module and LCD display panel imported into the United States each month, by or for you, and the amount of revenue generated each month for all such importations since May 13, 1999, including, for example, the per unit gross profit, net profit, and profit margin on each unit sold or transferred.

28. All documents reflecting the advertising, marketing, promotion of, and/or offer to sell in the United States since May 13, 1999, any types of LCD display modules and LCD display panels made, sold, or offered for sale, by or for you, since May 13, 1999, including, without limitation, brochures, marketing plans, sales literature, advertisements, and product descriptions and specifications.

29. Whether each type of LCD display module and LCD display panel made, sold, or offered for sale, by or for you, since May 13, 1999, could be lawfully imported and/or sold in the United States since May 13, 1999, including, for example, documents reflecting any United States regulatory approval and/or compliance.

**CERTIFICATE OF SERVICE**

The undersigned counsel certifies that, on December 14, 2005, he electronically filed the foregoing document with the Clerk of the Court using CM/ECF, which will send automatic notification of the filing to the following:

Robert W. Whetzel, Esq.  
Matthew W. King, Esq.  
Richards, Layton & Finger  
One Rodney Square  
P.O. Box 551  
Wilmington, DE 19899

The undersigned counsel further certifies that copies of the foregoing document were sent by email and hand to the above counsel and by email and first class mail to the following non-registered participants:

Christine A. Dudzik, Esq.  
Thomas W. Jenkins, Esq.  
Howrey LLP  
321 North Clark Street  
Suite 3400  
Chicago, IL 60610

Teresa M. Corbin, Esq.  
Glenn W. Rhodes, Esq.  
Howrey LLP  
525 Market Street  
Suite 3600  
San Francisco, CA 94105

/s/ Richard D. Kirk (rk0922)  
Richard D. Kirk

# Exhibit 7

# CONFIDENTIAL DOCUMENT

These pages have been removed because they contain confidential information subject to the Protective Order.

# Exhibit 8

## CONFIDENTIAL DOCUMENT

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# Exhibit 9

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# Exhibit 10

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# Exhibit 11

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# Exhibit 12

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# Exhibit 13

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# Exhibit 14

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# Exhibit 15

## CONFIDENTIAL DOCUMENT

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**CERTIFICATE OF SERVICE**

The undersigned counsel certifies that, on August 18, 2006, he electronically filed the foregoing document with the Clerk of the Court using CM/ECF, which will send automatic notification of the filing to the following:

Robert W. Whetzel, Esq.  
Matthew W. King, Esq.  
Richards, Layton & Finger  
One Rodney Square  
P.O. Box 551  
Wilmington, DE 19899

The undersigned counsel further certifies that copies of the foregoing document were sent on August 18, 2006 by email and by hand to the above counsel and by email and first class mail to the following non-registered participants:

Christine A. Dudzik, Esq.  
Thomas W. Jenkins, Esq.  
Howrey LLP  
321 North Clark Street  
Suite 3400  
Chicago, IL 60610

Teresa M. Corbin, Esq.  
Glenn W. Rhodes, Esq.  
Julie Gabler, Esq.  
Howrey LLP  
525 Market Street  
Suite 3600  
San Francisco, CA 94105

/s/ Richard D. Kirk (rk0922)  
Richard D. Kirk